

Engineering Science N2 01 April 2014

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As the co-founders of RISC-V, SiFive's level-up on processors could have significant implications for custom SoC design. Recently, a study into the architecture has been presented by members of the

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Marvell's OCTEON 10: The First DPU To Use Neoverse N2 ARMv9 Architecture

For NA we performed the analysis on human N1 and N2 versus avian NA. We compared 10 human H1, 3 human H2, and 293 human H3 with 95 avian HA sequences and found 13, 13, and 69 signatures (with ...

Genomic Signatures of Human Versus Avian Influenza A Viruses

To say that a lot has happened in the year since the COVID-19 pandemic started is an understatement of epic proportions, so much so that it may be hard to remember how the hardware hacking ...

A Simple But Effective High-Flow Oxygen Concentrator From Hardware Store Parts

We're all used to the humble LED as a ubiquitous source of light, but how many of us are aware that these components can also be used as photodiodes? It's something [Giovanni Blu Mitolo] takes ...

Optical Communication Using LEDs Alone

Dublin, July 19, 2021 (GLOBE NEWSWIRE) -- The "The Global Market for Nanostructured Coatings, Films and Surfaces (Nanocoatings) 2021-2031" report has been added to ResearchAndMarkets.com's offering.

Global Nanostructured Coatings, Films and Surfaces Market (2021 to 2031) - Key Drivers, Challenges and Opportunities

John von Neumann has revolutionized the world of computing through his work on the Manhattan Project, the von Neumann (Princeton) Architecture, and the first IAS computer. As the co-founders of RISC-V ...

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